


REMARKS

Original claims 1-8 have been canceled and new claims 9-14 have been added to incorporate the technical feature recited in original dependent claim 6 into independent apparatus and method claims.

Examination on the merits is requested.

Respectfully submitted,



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VERSION WITH MARKINGS TO SHOW CHANGES MADE

FIG. 1 is a block diagram showing a configuration of a demodulator related to an embodiment of the present invention;

[FIG. 2 is a explanatory view] FIGS. 2 (A)-(C) are explanatory views showing format conversion of TMCC signals in a demodulator related to an embodiment of the present invention;

[FIG. 3] FIGS. 3 (A)-(H) is a timing chart provided for explanation on operation of a demodulator related to an embodiment of the present invention;

FIG. 4 is a block diagram showing a configuration of a conventional demodulator;

FIG. 5 is a truth table of control signals in a demodulator;

[FIG. 6] FIGS. 6 (A)-(B) is a timing chart provided for explanation on operation of a prior art demodulator; and

[FIG. 7 is an explanatory view] FIGS. 7 (A)-(D) are explanatory views of a technical concept of Viterbi decoding.